Docket No.: 50090-233

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Yukihiro NAGAI, et al.

Serial No.: 09/631,623

Filed: August 04, 2000

Group Art Unit: 2812

Examiner: M. Kirk

SEMICONDUCTOR DEVICE HAVING A POTENTIAL FUSE, AND METHO

OF MANUFACTURING THE SAME

AMENDMENT

Commissioner for Patents Washington, DC 20231

Sir:

For:

The following Amendment and Remarks are submitted in response to the Office Action dated October 4, 2002.

IN THE CLAIMS:

Please amend claims 1, 4 and 7 as follows.

- 1. (Twice Amended) A semiconductor device comprising:
- a semiconductor substrate;
- a first portion comprising a plurality of active regions formed in the semiconductor substrate;
 - a plurality of isolation regions separating the active regions from each other;
- a second [section] portion comprising at least one trench having an interior surface formed by side surfaces and a bottom surface;